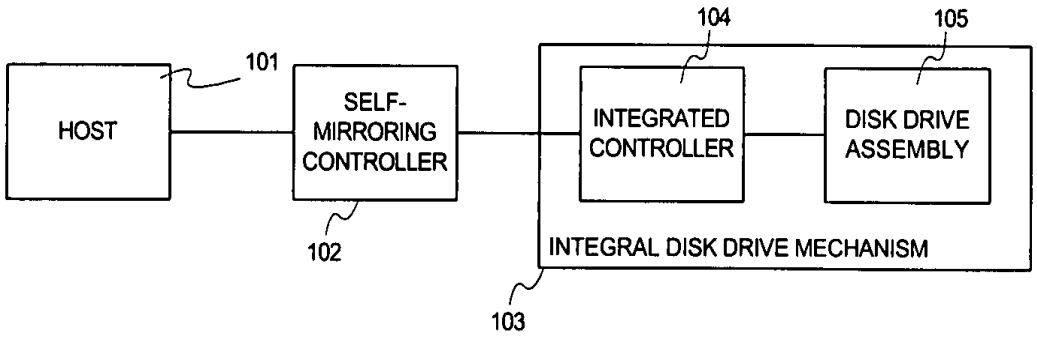
HOST
101
SELF-
MIRRORING
CONTROLLER
102
104
INTEGRATED
CONTROLLER
105
DISK DRIVE
ASSEMBLY
INTEGRAL DISK DRIVE MECHANISM
103

FIG. 1A

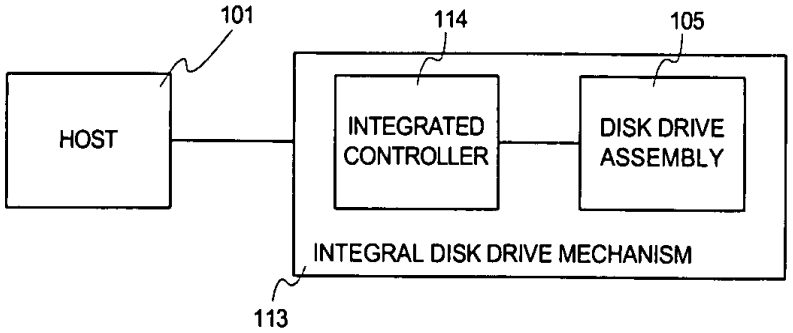
HOST
101
114
INTEGRATED
CONTROLLER
105
DISK DRIVE
ASSEMBLY
INTEGRAL DISK DRIVE MECHANISM
113

FIG. 1B

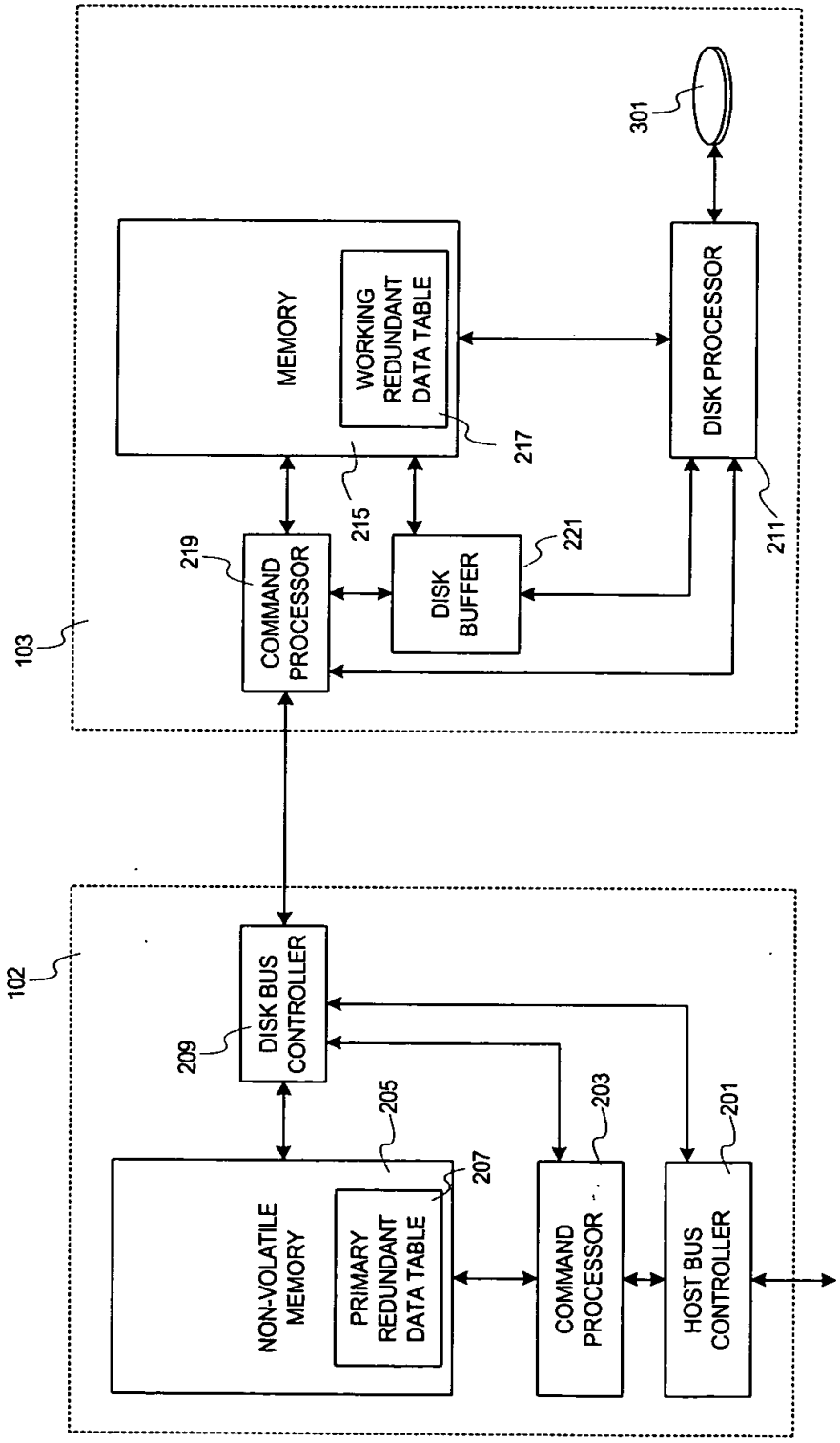
301
MEMORY
WORKING REDUNDANT DATA TABLE
217
DISK PROCESSOR
215
211
219
COMMAND PROCESSOR
DISK BUFFER
221
103
102
209
DISK BUS CONTROLLER
205
207
203
201
NON-VOLATILE MEMORY
PRIMARY REDUNDANT DATA TABLE
COMMAND PROCESSOR
HOST BUS CONTROLLER

FIG. 2

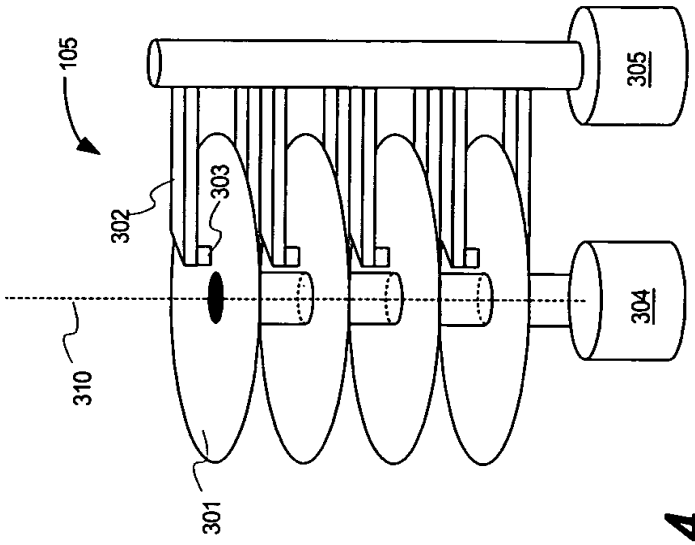

FIG. 4

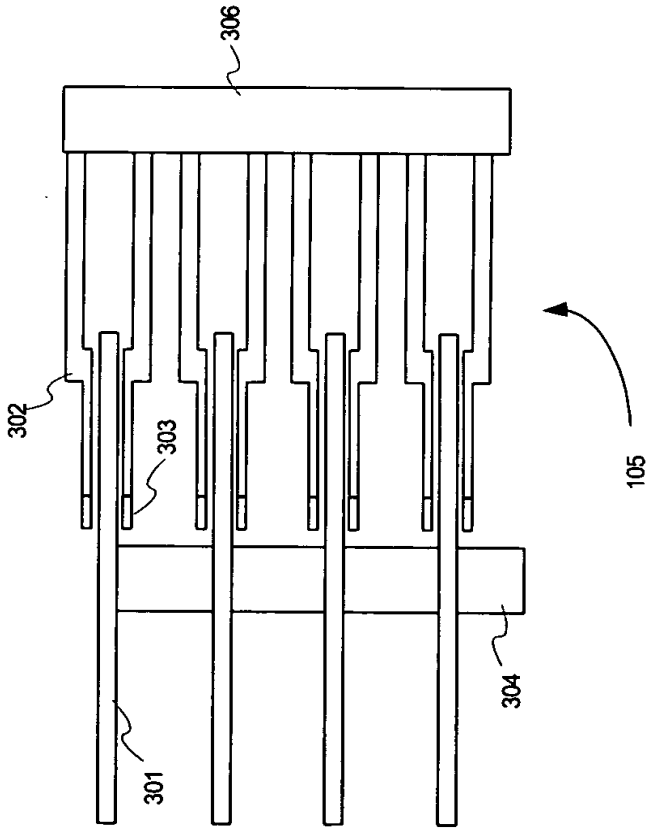

FIG. 3

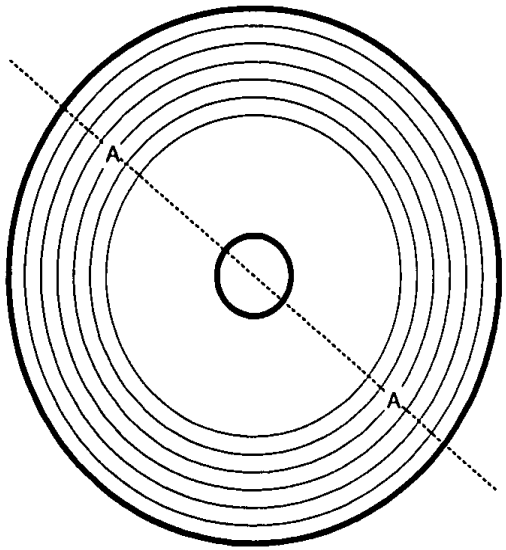

FIG. 5A

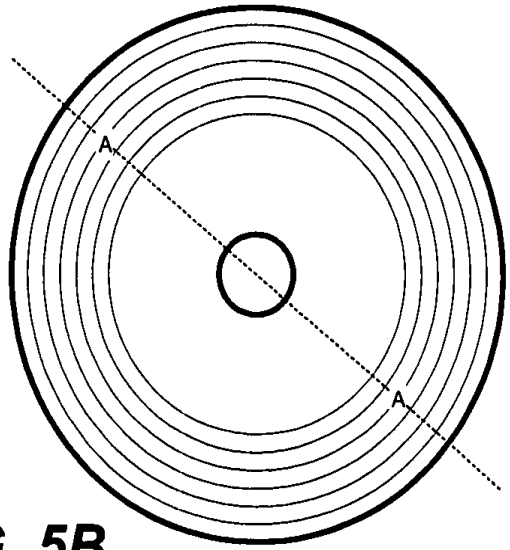

FIG. 5B

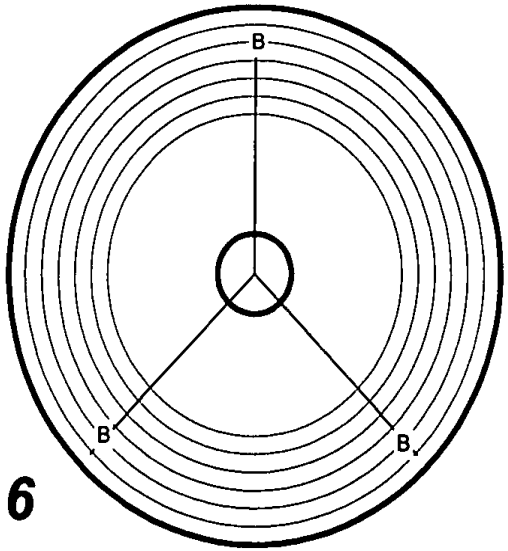

FIG. 6

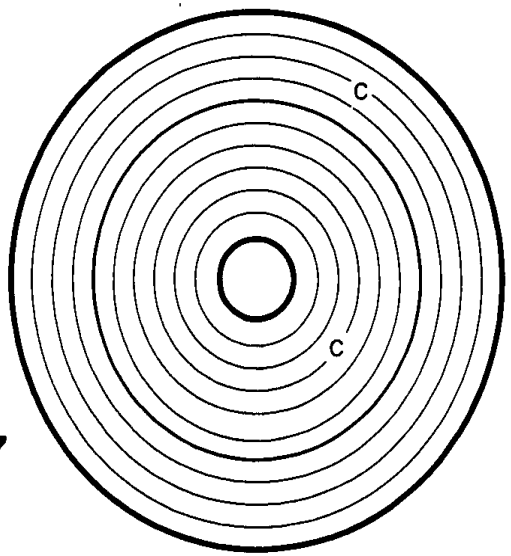

FIG. 7

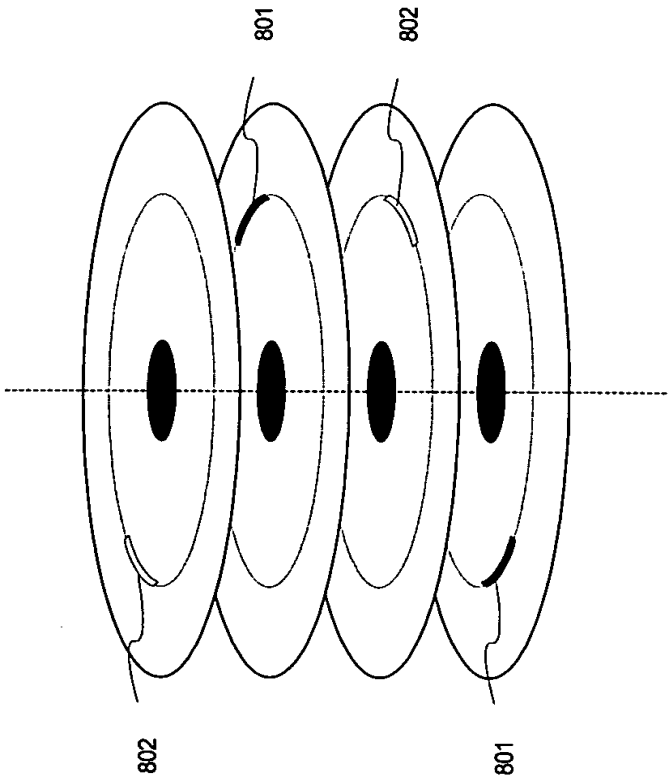

FIG. 8

Toshiba Emission

	7	6	5	4	3	2	1	0
0	Operation Code (C8h)							
1	Reserved			DPO	FUA	Reserved		RELADR
2 – 5	Logical Block Address							
6	Replicate Number							
7 – 8	Transfer Length							
9	Control							

FIG. 9

	7	6	5	4	3	2	1	0
0	Operation Code (C9h)							
1	Reserved					RRDE	RDEOnly	FRC
2 – 6	Reserved							
7 – 8	Transfer Length (0Eh)							
9	Control							

FIG. 10

	7	6	5	4	3	2	1	0
0	Redundant Data Entry Length (0Dh)							
1	NoRDE	Reserved						
2 – 5	Logical Block Address							
6	Replicate Source							
7	Replicate Number							
8 – 9	Transfer Length							
10 – 13	Replicate Identifier							

FIG. 11

	7	6	5	4	3	2	1	0
0	Operation Code (53h)							
1	Reserved	Reserved	Disable XOR	DPO	FUA	Disable Write	Reserved	Reserved
2 – 5	Logical Block Address							
6	Reserved							
7 – 8	Transfer Length							
9	Control							

FIG. 12